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Gustavo Siller, Jr., Reg. No. 32,305

Name of applicant, assignee, or Registered Pepresentative

Signature

Date of Signature

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Our File No. 9281-4784

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)
Masaki Yamamoto)
Patent No.: 7,190,414 B2) Examiner: Trang U. Tran
Issue Date: March 13, 2007	Group Art Unit: 2622
Serial No.: 10/811,698)
Filing Date: March 29, 2004)
For: Input Circuit Of Television Tuner In Interference Caused By Fm Broadcas Signal Is Attenuated	\

REQUEST FOR CERTIFICATE OF CORRECTION

Certificate

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 MAY 0 1 2007

Attn: Certificate of Corrections Branch

or Correction

Sir:

Please issue a Certificate of Correction for the above-identified patent to correct the errors listed on the accompanying PTO Form 1050. This request is being made pursuant to 35 U.S.C. § 254, since the errors comprise minor or typographical errors by the Patent and Trademark Office.

The Commissioner is hereby authorized to charge any fees required to Deposit Account No. 23-1925. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

Gustavø Siller, Jr. Registration No. 32,305 Attorney for Applicant

BRINKS HOFER GILSON & LIONE P.O. Box 10395 Chicago, Illinois 60610 (312) 321-4200

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PATENT NO

7,190,414 B2

APPLICATION NO.:

10/811,698

ISSUE DATE

March 13, 2007

INVENTOR(S)

Masaki Yamamoto

It is certified that error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the Title Page

In column 2, delete lines 1-22 under "ABSTRACT" and substitute -- In an FM/TV receiver, a first input terminal is connected to an input end of an input tuning circuit, first and second diodes are serially connected between a second input terminal and the input end of the input tuning circuit and are simultaneously switched on or off, the first diode is input to the input end of the input tuning circuit, the second diode is provided at the side of the second input terminal, an inductance element is interposed between the second input terminal and the second diode, and a first capacitance element that forms a low pass filter together with the inductance element in a state when the second diode is switched on, is connected between the connection point of the first diode and the second diode and a ground.-- in its place.

MAILING ADDRESS OF SENDER (Please do not use customer number below):

PATENT NO.

7,190,414 B2

Gustavo Siller, Jr.

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P.O. Box 10395

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